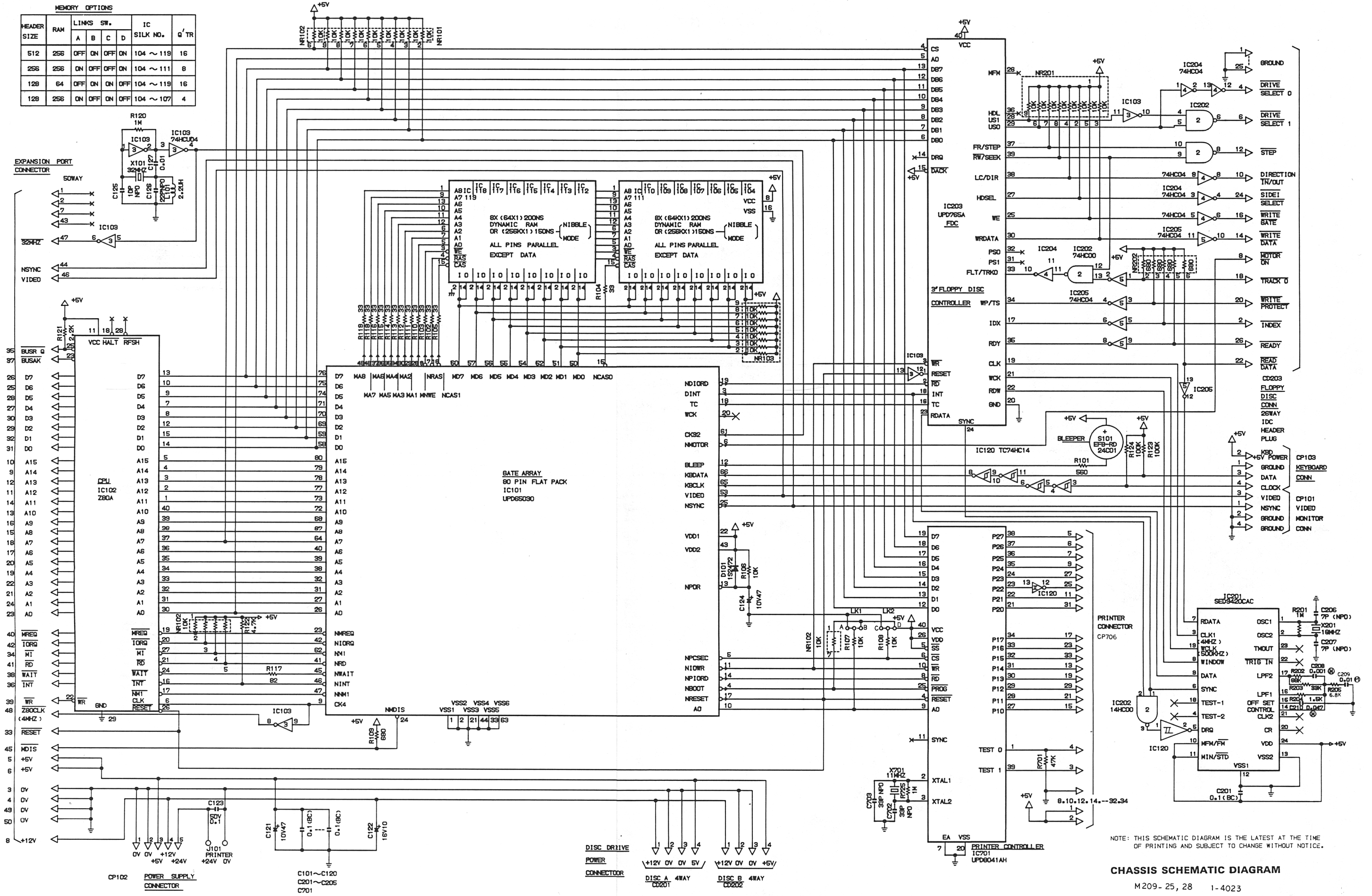


CHASSIS SCHEMATIC DIAGRAM

MEMORY OPTIONS

HEADER SIZE	RAM	LINKS SW.				IC SILK NO.	Q' TR
		A	B	C	D		
512	256	OFF	ON	OFF	ON	104 ~ 119	16
256	256	ON	OFF	OFF	ON	104 ~ 111	8
128	64	OFF	ON	ON	OFF	104 ~ 119	16
128	256	ON	OFF	ON	OFF	104 ~ 107	4



NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

CHASSIS SCHEMATIC DIAGRAM