

SERVICE BULLETIN

from: TRIO-KENWOOD COMMUNICATIONS, INC.

TR-7800

841

SUBJECT: TR-7800 Clock and Display Failure

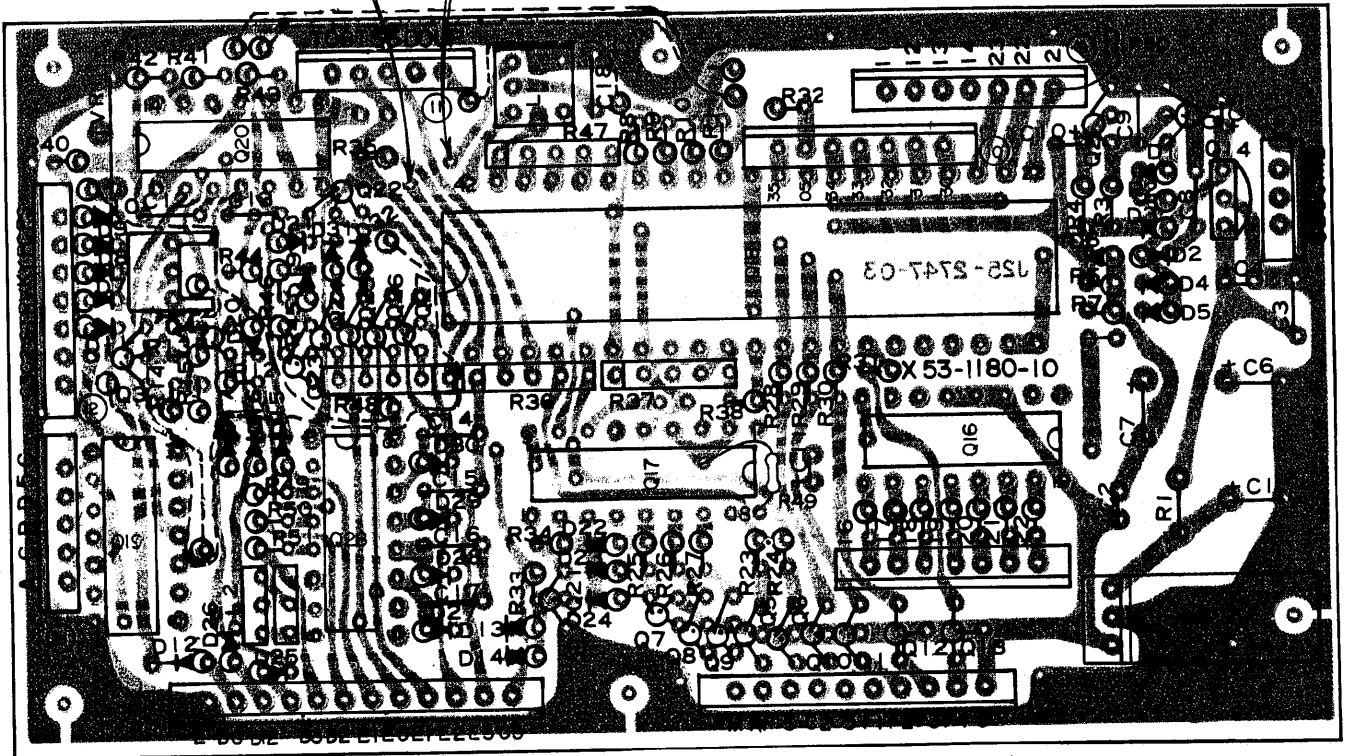
DATE 01/09/81

Failure of the display may be caused by an intermittent via-hole (through-hole without a lead) in the microprocessor clock circuit. Hard-wiring the holes will cure this problem.

On the Control unit X53-1180-xx, hard-wire the two via-holes as shown.

PC BOARD VIEWS

▼ CONTROL UNIT (X53-1180-XX) XX: 10(K), 61(W)(T)



JEB/jeb

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